

Error mitigation for partially error-corrected quantum computers

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We present a method for quantum error mitigation on partially error-corrected quantum computers — i.e., computers with some logical qubits and some noisy qubits. Our method is inspired by the error cancellation method and is implemented via a circuit for convex combinations of channels which we introduce in this work. We show how logical ancilla qubits can arbitrarily reduce the sampling complexity of error cancellation in a continuous space-time tradeoff, in the limiting case achieving $O(1)$ sample complexity which circumvents lower bounds for sample complexity with all known error mitigation techniques. This comes at the cost of exponential circuit depth, however, and leads us to conjecture that any error mitigation protocol with (sub-)polynomial sample complexity requires exponential time and/or space, even when logical qubits are utilized as a resource. We anticipate additional applications for our quantum circuits to implement convex combinations of channels, and to this end we discuss one application in simulating open quantum systems, showing an order of magnitude reduction in gate counts relative to current state-of-the-art methods for a canonical problem.

Introduction — Noise in quantum computers necessitates circuit-level methods, conventionally known as quantum error correction and quantum error mitigation, to produce accurate and reliable results. Quantum error correction (QEC) is the holy grail and has seen significant recent progress [1–5], but the relatively high overhead required for QEC has motivated lower overhead techniques known as quantum error mitigation (QEM) in recent years [6–8]. While QEC and QEM are often viewed as two distinct strategies for two distinct frameworks of quantum computing — namely, noisy intermediate-scale quantum (NISQ) [9] computing and fault-tolerant quantum computing (FTQC) — the transition from NISQ to FTQC is unlikely to be a sudden phase transition. Rather, it will likely be a gradual transition from noisy to logical qubits, with future devices capable of correcting errors on some but not all qubits. To this end, in this work we consider the setting of *partially error-corrected* quantum computers with some logical (perfect) qubits and some physical (noisy) qubits, and we develop a method for executing accurate quantum computations. Our method is inspired by a QEM technique known as probabilistic error cancellation (PEC), and works via a circuit construction for implementing convex combinations of channels that we introduce in this work. This circuit construction may be of independent interest and find use in other applications — for example we also discuss the application of simulating open quantum systems.

The QEM technique known as probabilistic error cancellation (PEC), and sometimes the quasi-probability distribution (QPD), method [10] has received considerable attention [7, 11–14] due to its relatively simple procedure as well as its ability to produce exact results, assuming

noisy operations can be perfectly characterized. Like several other error mitigation techniques, however, the time to implement the method grows exponentially in some parameter [15]. In particular, the time complexity (number of circuits needed to be executed) of PEC scales as Γ^L where Γ is the overall negativity and L is the number of operations in the circuit. (More detail below.) Because of this, several authors have sought methods to lower the negativity Γ in recent literature [16–18], either by finding optimal unitary representations, combining PEC with other techniques, or allowing for local operations and classical communication (LOCC).

In the setting of partially error-corrected computers, we show that our method is able to reduce the negativity (equivalently, the sample complexity) arbitrarily. While this reduction in sample complexity comes at the cost of space complexity — namely additional qubits and additional operations — we find this an interesting theoretical result because it bypasses lower bounds on sample complexity required for error mitigation [15, 19, 20]. In particular, Theorem 1 of [20] shows that any (weak) quantum error mitigation algorithm requires a number of samples proportional to $p^{-\Omega(nD)}$ where p is the local depolarizing noise rate, n is the number of qubits, and D is the depth of the circuit. In contrast, in the limiting case of our method the sample complexity is $O(1)$ on partially error-corrected quantum computers.

Preliminaries — We begin, as in PEC, by assuming perfect knowledge of the noisy implementable operations $\{\mathcal{O}_\alpha\}_{\alpha=1}^M$ on a given quantum computer. We further assume that these operations form a basis such that any

unitary channel $\mathcal{U}$ can be written

$$\mathcal{U} = \sum_{\alpha} c_{\alpha} \mathcal{O}_{\alpha} = \gamma \sum_{\alpha} \sigma_{\alpha} p_{\alpha} \mathcal{O}_{\alpha}. \quad (1)$$

Here, c_{α} are real coefficients, and in the second equality we have defined $\sigma_{\alpha} := \text{sgn}(c_{\alpha})$ and $p_{\alpha} := |c_{\alpha}|/\gamma$ with $\gamma := \sum_{\alpha} |c_{\alpha}|$. This allows us to view the equality as a quasi-probability distribution with negativity γ . For a product of L unitaries $\mathcal{C} := \mathcal{U}_L \cdots \mathcal{U}_1$ — i.e., a depth L quantum circuit — we can repeatedly apply (1) to obtain

$$\mathcal{C} = \Gamma \sum_{\alpha} \sigma_{\alpha} p_{\alpha} \mathcal{O}_{\alpha} \quad (2)$$

where now the bold-faced index α runs over the tuple $\alpha := (\alpha_1, \dots, \alpha_L)$ and the overall negativity $\Gamma := \gamma^{[1]} \dots \gamma^{[L]}$ is the product of negativities $\gamma^{[i]}$ for each unitary $\mathcal{U}_i$. For an observable $A = A^{\dagger}$ and initial state ρ , we can now write

$$\langle A \rangle \equiv \text{Tr}[\mathcal{C}(\rho)A] = \Gamma \sum_{\alpha} \sigma_{\alpha} p_{\alpha} \text{Tr}[\mathcal{O}_{\alpha}(\rho)A]. \quad (3)$$

We emphasize this expression is exact and allows us to write an ideal expectation value as a linear combination

of noisy expectation values. Let us refer to this as error cancellation. The technique of PEC is to sample $\mathcal{O}_{\alpha}$ according to p_{α} and compute

$$\langle A \rangle_{\text{PEC}}(N) := \frac{1}{N} \sum_{i=1}^N \sigma_{\alpha} \text{Tr}[\mathcal{O}_{\alpha}(\rho)A]. \quad (4)$$

Letting $\langle A \rangle$ denote the ideal (noiseless) expectation value, to achieve $|\langle A \rangle_{\text{PEC}}(N) - \langle A \rangle| \leq \delta$, we execute $N = O(\Gamma^2/\delta^2)$ quantum circuits [10]. Letting $\gamma := \max_{1 \leq i \leq L} \gamma_i$, we have that $\Gamma = O(\gamma^L)$ and thus $N = O(\gamma^{2L}/\delta^2)$, showing the importance of minimizing γ (provided that L is fixed by the quantum circuit of interest) for practical applications. Ref. [16] explored reducing Γ by extending the set of implementable operations $\{\mathcal{O}_{\alpha}\}_{\alpha}$ via noise scaling; Ref. [17] explored reducing Γ by allowing for approximation error and introducing an algorithm for choosing the quasi-probability distribution in a noise-aware manner; and Ref. [18] explored reducing Γ by grouping unitaries to decrease the total number of operations L in the circuit.

Results — We write (3) as

$$\langle A \rangle = \Gamma^{[+]} \text{Tr} \left[\left(\sum_{\sigma_{\alpha}=+1} p_{\alpha} \mathcal{O}_{\alpha}(\rho) \right) A \right] - \Gamma^{[-]} \text{Tr} \left[\left(\sum_{\sigma_{\alpha}=-1} p_{\alpha} \mathcal{O}_{\alpha}(\rho) \right) A \right]. \quad (5)$$

Here, we have grouped terms according to their signs $\sigma_{\alpha} = \pm 1$ and defined coefficients $\Gamma^{[\pm]} := \prod_{\sigma_i=\pm 1}^L \gamma^{[i]}$. Note that each of these two terms represents an expectation value computed with respect to a convex combination of channels $\sum_{\alpha} p_{\alpha} \mathcal{O}_{\alpha}$. By developing quantum circuits for convex combination of channels, which utilize logical ancilla qubits, we show how to evaluate expressions like (5) in partially error-corrected quantum computers to perform error cancellation. In particular, we show how logical ancilla qubits can reduce the negativity of error cancellation, as stated in the following theorem.

Theorem 1 (Error cancellation on partially error-corrected quantum computers). *Let $\mathcal{C} := \mathcal{U}_L \cdots \mathcal{U}_1$ be an n -qubit, depth L quantum circuit preparing the state ρ , and let A be an observable. Provided $O(k)$ logical qubits, there exists an error cancellation protocol to estimate $\text{Tr}[\rho A]$ with negativity $O(\gamma^{L-k})$, where $\gamma = \max_{1 \leq i \leq L} \gamma(U_i)$ and $\gamma(U_i) := \sum_{\alpha} |c_{\alpha}|$ is the negativity of the i th unitary (see (1)). Equivalently, there exists an error cancellation protocol using $O(k)$ logical qubits which estimates $\text{Tr}[\rho A]$ to accuracy δ with sample complexity $O(\gamma^{2(L-k)}/\delta^2)$.*

This result provides one perspective as to how QEM and QEC can fit together in the transition from NISQ to FTQC. The power of each additional logical qubit in

this setting is that it can be used to reduce the negativity of PEC — as mentioned a result sought by several authors [16–18].

Recall that recent work (namely, Theorem 1 of [20]) has shown that any (weak) quantum error mitigation algorithm requires a number of samples proportional to $p^{-\Omega(nD)}$ where p is the local depolarizing noise rate, n is the number of qubits, and D is the depth of the circuit. In the extreme case of using $k = O(L)$ logical qubits, we see from Theorem 1 that the negativity becomes $O(1)$ and thus the sample complexity becomes $O(1/\delta^2)$ for target accuracy δ . In principle this bypasses the exponential sample complexity lower bound via the use of logical ancilla qubits, however the method is not practical due to two reasons. First, we must require that $n > L$, for if we had enough logical qubits there would be no need to perform error cancellation. Second, as will be shown in the method, we require (partially error-corrected) circuits of depth $O(\exp L)$. For future reference we state this as a corollary:

Corollary 1 (Constant-sample error cancellation on partially error-corrected quantum computers, at the cost of exponential depth). *Let $\mathcal{C} := \mathcal{U}_L \cdots \mathcal{U}_1$ be an n -qubit, depth L quantum circuit preparing the state ρ , and let A be a Hermitian operator. Provided $O(L)$ logical ancilla*

qubits and n noisy qubits, there exists a protocol to estimate $\text{Tr}[\rho A]$ to accuracy δ using $O(1/\delta^2)$ samples and two circuits of depth $O(\exp L)$.

Although this result is not practical in its current form, it is notable that it overcomes the best bounds on sample complexity for error mitigation. Notably, Ref. [6] reviews the main quantum error mitigation techniques which have been developed thus far, and in Table IV enumerates the qubit overhead, circuit runtime overhead, and sampling overhead for them. For the techniques of probabilistic error cancellation, Richardson extrapolation, symmetry verification, virtual distillation, and echo verification, one can see that the sampling overheads all grow exponentially in some parameter, typically the error rate of the computer and/or size of the problem. The difference between our technique is the introduction of *logical* qubit overhead to achieve constant sample complexity in the limiting case. While the exponential depth makes this method impractical, we conjecture (see *Conclusions*) that this is not a deficiency of our approach but rather a fundamental feature of error mitigation. In short, we conjecture there is “no free lunch” in error mitigation even when using additional (logical) qubits.

Besides this corollary, we believe that Theorem 1 will provide the most practical applications of our procedure to reduce the sample complexity of error cancellation using an intermediate number $1 < k < L$ of logical qubits. We now begin proving these results, beginning with our quantum circuit for implementing convex combinations of channels which underlies both.

Methods: Quantum circuits for convex combinations of channels — Various authors have considered quantum circuits for implementing (single) quantum channels [21–24], a technique which has manifold applications in the evolution of open quantum systems [25]. A limited case of combinations of channels is presented in [23] where linear combinations of single-qubit depolarizing channels are taken. Our work generalizes this to arbitrary convex combinations of quantum channels.

Note that the linear combination of channels is not necessarily a quantum channel. For example, the single-qubit map $\mathcal{E}(\rho) := \mathcal{E}_0(\rho) - \mathcal{E}_1(\rho)$ where $\mathcal{E}_\alpha := \rho \mapsto |\alpha\rangle\langle\alpha|$ is not (completely) positive. On the other hand, if $\mathcal{E}_\alpha$ are completely positive and trace-preserving (CPTP) maps and $p_\alpha \geq 0$ are real coefficients which sum to unity, then $\mathcal{E} := \sum_\alpha p_\alpha \mathcal{E}_\alpha$ is a CPTP map. Indeed, preservation of trace follows from $\text{Tr}[\mathcal{E}] = \sum_\alpha p_\alpha \text{Tr}[\mathcal{E}_\alpha] = \sum_\alpha p_\alpha = 1$, and (complete) positivity follows from linearity with (complete) positivity of each $\mathcal{E}_i$.

The quantum circuit in Fig. 1 proves the following:

Theorem 2 (Quantum circuit to implement a convex combination of channels). *For an n -qubit state ρ , there*

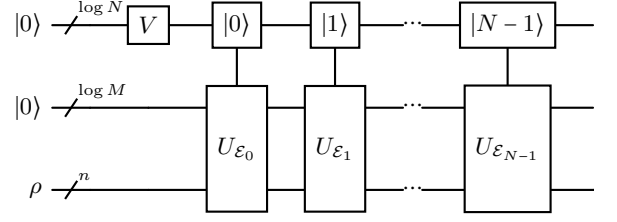


FIG. 1. A quantum circuit to implement the convex combination of channels (6). The procedure is similar to the LCU technique with unitary dilations $U_{\mathcal{E}_\alpha}$ for each channel $\mathcal{E}_\alpha$, however there is no post-selection and the preparation is deterministic. The unitary V prepares the probability distribution $V|0\rangle = \sum_\alpha \sqrt{p_\alpha}|\alpha\rangle$ and gates labeled $|\alpha\rangle$ on the top register represent operations controlled on the state $|\alpha\rangle$. The second register used for channel dilations requires $\log M$ qubits, where $M = \max_\alpha M_\alpha$ and M_α is the number of Kraus operators in channel $\mathcal{E}_\alpha$. At the end of the circuit, the bottom register has the convex combination of channels applied to the input state ρ , i.e. $\sum_\alpha p_\alpha \mathcal{E}_\alpha(\rho)$, as proved in the main text.

exists a quantum circuit (Fig. 1) to implement

$$\rho \mapsto \sum_{\alpha=1}^N p_\alpha \mathcal{E}_\alpha(\rho) \quad (6)$$

where $p_\alpha \geq 0$ and $\mathcal{E}_\alpha$ are CPTP maps. This circuit uses $n + \log N + \log M$ qubits and has depth $|V| + N$, where $|V|$ is the depth of the unitary V which prepares the coefficient distribution p_α , and M is the maximum number of Kraus operators in the channels $\mathcal{E}_\alpha$. The quantum circuit succeeds deterministically, implementing the map (6) with probability one.

Before proving this result, we note that the technique is similar to the linear combinations of unitaries (LCU) [26–28] technique, but succeeds deterministically. In particular, the LCU technique implements $|\psi\rangle \mapsto \sum_{\alpha=1}^N c_\alpha U_\alpha |\psi\rangle$ by block encoding and post-selection. Here, a unitary V is used to prepare the (normalized) coefficients $\sum_\alpha \sqrt{c_\alpha}|\alpha\rangle$ from the $|0\rangle$ state on $\log N$ qubits. Subsequently, controlled U_α operations are used to prepare $\sum_\alpha c_\alpha |\alpha\rangle U_\alpha |\psi\rangle$. The coefficient register is then uncomputed such that post-selecting this register on $|0\rangle$ prepares $\sum_\alpha c_\alpha U_\alpha |\psi\rangle$. Because of this, LCU succeeds with a probability that depends on measuring $|0\rangle$ in the coefficient register. This probability depends on the coefficients c_α , but can be amplified via amplitude amplification [29]. In contrast, our construction succeeds deterministically.

Proof. It is well known that any CPTP map $\mathcal{E}$ can be written as a unitary acting on a larger Hilbert space followed by a partial trace [30]. For example, the Stinespring dilation of a channel $\mathcal{E}(\rho) := \sum_{i=1}^M K_i \rho K_i^\dagger$ is given by the unitary $U_\mathcal{E}$ such that

$$\langle a, i | U_\mathcal{E} | b, 0 \rangle = \langle a | K_i | b \rangle. \quad (7)$$

Here, $|a\rangle$ and $|b\rangle$ index basis states for the system register H_S and $|i\rangle$ indexes basis states for the environment register H_E which has maximum dimension $\dim H_E \leq \dim^2 H_S$. Other unitary dilations include the Sz.-Nagy dilation [24] as well as symmetric and antisymmetric exponentials as in [21].

To implement the convex combination of channels $\sum_\alpha p_\alpha \mathcal{E}_\alpha$ where each channel has Kraus operators $K_{1,\alpha}, \dots, K_{M_\alpha,\alpha}$, we apply a unitary dilation to each channel $\mathcal{E}_\alpha \mapsto U_{\mathcal{E}_\alpha}$ and proceed as in Fig. 1. To see this circuit implements the desired convex combination of channels, assume for simplicity that $\rho = |\psi\rangle\langle\psi|$ is a pure state. Then, the final state is

$$|\Psi\rangle = \sum_{\alpha=1}^N \sum_{j=1}^{M_\alpha} \sqrt{p_\alpha} |\alpha\rangle |j\rangle K_{j,\alpha} |\psi\rangle \quad (8)$$

Taking the partial trace over the first (top) and second (middle) registers gives the reduced density matrix

$$\rho = \text{Tr}_{1,2}[|\Psi\rangle\langle\Psi|] = \sum_{\alpha=1}^N \sum_{j=1}^{M_\alpha} p_\alpha K_{j,\alpha} |\psi\rangle\langle\psi| K_{j,\alpha}^\dagger \quad (9)$$

by virtue of the states $|\alpha\rangle$ for the first register and $|j\rangle$ for the second register being orthonormal. Extension from pure state input to mixed state input follows by linearity. $\square$

Note that the controlled operations enable us to reuse the same ancillary qubits for each channel dilation. It is also possible to introduce new ancilla qubits between each dilation, either through measure-and-reset of the previous ancilla or measurement of the previous ancillae and introduction of new ancillae.

Application 1: Error cancellation — It is now straightforward to prove Theorem 1 and Corollary 1. The two terms of the error cancellation method (5) are both convex combinations of channels, and so can be evaluated exactly via two applications of the circuit in Fig. 1. The total number of terms in (5) is $N = O(\exp L)$, and therefore we require $O(L)$ logical qubits in the top register of Fig. 1. Note that the second register of $\log M$ qubits is not necessary for error cancellation because we assume the operations themselves are noisy channels. When controlling operations on logical qubits (register C) acting on the target system (register S), we assume that the noise acts as

$$\rho_{CS} \mapsto \sum_{\alpha} (I_C \otimes K_{\alpha}) \rho_{CS} (I_C \otimes K_{\alpha})^\dagger. \quad (10)$$

This assumption is natural since any errors which occur on logical qubits, or which occur on physical qubits and spread to logical qubits, will be able to be corrected on the logical qubits. Under this assumption, it is sufficient to characterize the noisy implementable operations on the noisy system, and use these characterizations in the error cancellation procedure. The only subtlety is

that a controlled implementable operation may not be directly implementable. For example, on a computer with gateset $\{U, \text{CNOT}\}$ with U being any single-qubit unitary, CZ is not a directly implementable. This subtle issue can be resolved by simply compiling the operation into a sequence implementable gates, then characterizing this sequence. For example, we would compile CZ to $(I \otimes H)\text{CNOT}(I \otimes H)$, then characterize this sequence. This completes the proof of Corollary 1.

The proof of Theorem 1 follows the same line of reasoning, just applying the circuit for a convex combination of channels to a subset of operations and sampling from the remaining operations as in PEC. For example, let $\mathcal{C} = \mathcal{U}_2 \mathcal{U}_1$ with $\mathcal{U}_i$ as in (1). Rather than implement both unitaries via convex combinations of channels, we implement only one of them via convex combinations of channels, and sample operations from the other. That is, we can write

$$\begin{aligned} \text{Tr}[\rho A] &= \gamma^{[2]} \sum_{\alpha} \sigma_{\alpha}^{[2]} p_{\alpha}^{[2]} \times \\ &(\gamma^{[1,+]} \text{Tr}[(\mathcal{O}^{[1,+]} \circ \mathcal{O}_{\alpha})(\rho) A] - \gamma^{[1,-]} \text{Tr}[(\mathcal{O}^{[1,-]} \circ \mathcal{O}_{\alpha})(\rho) A]) \end{aligned}$$

where $\mathcal{O}^{[1,\pm]} := \sum_{\sigma_{\alpha}=\pm 1} p_{\alpha}^{[1]} \mathcal{O}_{\alpha}$ is implemented via Fig. 1 and the remaining term is implemented by sampling $p_{\alpha}^{[2]}$ as in PEC. Letting $\gamma^{[1]} = \gamma^{[2]} = \gamma$ as before, we see directly from this expression that negativity of PEC is reduced by a factor of γ . Note that in this case $O(1)$ logical qubits are required to implement the circuit in Fig. 1. The generalization to $O(k)$ logical qubits follows directly.

Application 2: Simulating open quantum systems — While we have focused on the application of error cancellation, there are likely additional applications of our circuit for convex combinations of channels. Here we describe one, namely the simulation of open quantum systems. In the study of open quantum systems, one commonly uses the Born and Markov approximations to simplify model-building. Any physics not in agreement with the Born and Markov approximations is commonly said to be non-Markovian. The general treatment of non-Markovian open systems often involves integrodifferential master equations [31, 32], approximate time-convolutionless master equations [33], or approaches which emulate the bath using a smaller, ancillary system [34, 35]. Recent studies have found special cases where non-Markovian dynamics can come from convex combinations of Markovian channels [36–38]. Such constructions allow the implementation of non-Markovian dynamics without the need to explicitly simulate the environment, or other exponential overhead. Additionally, Ref. [39] implements convex mixtures of single-qubit Pauli channels to simulate both Markovian and non-Markovian dynamics. Finally, Ref. [36] discusses the role of CP divisibility in determining whether a channel is Markovian or non-Markovian. Our circuit construction for implementing convex combinations of channels thus

Method	Qubits	Two-qubit gates	Circuit depth
Refs. [40, 42]	8	1106	2197
This work	4	126	253

TABLE I. Qubits, two-qubit gates, and circuit depths for simulating damped Rabi flopping (12) with the method of Ref. [40, 42] and with our method for implementing convex combinations of channels (Fig. 1).

presents an avenue for both simulating (non-)Markovian dynamics and to test if a given evolution expressed as a convex combination of channels is (non-)Markovian.

To illustrate one application and the advantage of our approach, consider the Lindblad equation

$$\dot{\rho} = -i[H, \rho] + \sum_k \Gamma_k \left(L_k \rho L_k^\dagger - \frac{1}{2} \{L_k, L_k^\dagger, \rho\} \right), \quad (11)$$

a master equation for Markovian open quantum systems commonly employed in atomic, molecular, and optical physics, among other applications. Recent work [40] has identified a technique for solving the Lindblad equation on a quantum computer that casts the problem as a convex combination of channels, similar to the quantum stochastic drift protocol used for unitary evolution [41]. While this convex combination of channels can be implemented by classically sampling the channels, the work by David et al. [40] proposes using the quantum forking protocol [42] to implement the combination with a single quantum circuit. The quantum forking protocol requires multiple registers, one for each channel, each of which is the size of the system the channels are being applied to. However, our construction (Fig. 1) uses the same register to implement an arbitrary convex combination of channels. Additionally, quantum forking requires that a deep network of controlled swap gates be applied to the system registers before and after the channels are applied. This must be done for each time step in the solution to Lindblad equation. As a tradeoff, our method requires the channels to be controlled. To get a sense of the advantage of our approach, we considered a simple canonical problem of a two level atom subject to damped Rabi flopping. This system is described by the Lindbladian

$$\dot{\rho} = -i[\rho, H] + \gamma(\sigma^- \rho \sigma^+ + \frac{1}{2} \{\sigma^- \sigma^+, \rho\}), \quad (12)$$

where $H = \omega_0 \sigma_Z + \Omega \sigma_X$, σ_X and σ_Z are the Pauli matrices, and $\sigma^- = |g\rangle\langle e|$, $\sigma^+ = |e\rangle\langle g|$. Compared to the quantum forking method, our circuit requires roughly an order of magnitude fewer gates, as shown in Table I. More details on this comparison are given in the Appendix.

Conclusion — In this Letter, we have introduced a method for implementing error cancellation on partially error-corrected quantum computers, showing how logical qubits can reduce the sample complexity of probabilistic error cancellation. In the theory of error mitigation, our

work bypasses previous results showing any error mitigation algorithm must consume an exponential number of samples. Indeed, we have discussed how the extreme limit of our method allows for error cancellation with constant sample complexity. While this requires exponential space (gate depth) overhead to do, we find it unlikely that any error mitigation procedure will achieve “a free lunch” with polynomial time, space, and sample complexity. Indeed, as shown in Table IV of Ref. [6], all known error mitigation techniques with constant or polynomial space and time resources require exponential sample complexity. In light of these results and our results in Theorem 1 and Corollary 1, we conjecture:

Conjecture 1. *Any error mitigation technique achieving (sub-)polynomial sample complexity must require exponential space and/or time complexity, even when logical qubits are utilized as a resource.*

Nonetheless, we believe Theorem 1 will provide a useful error mitigation protocol as we begin to have quantum computers with some logical qubits. This protocol is interesting in that it shows one method in which QEM and QEC can work together, utilizing QEC to reduce the (sampling) overhead of QEM. We emphasize that, when targeting practical applications, known methods to reduce the overhead of error cancellation can be applied for our protocol as well — e.g., grouping operators to reduce the total number of gates L in the circuit, tailoring device noise by Pauli twirling to simplify unitary representations, dropping terms with small coefficients to produce an approximate result with lower overhead, and even combining the protocol with other methods like zero-noise extrapolation to reduce the negativity further. It may even be possible to perform our method with noisy qubits in place of logical qubits. This would likely require noise tailoring on the top register of Fig. 1 and careful consideration of how implementable operations in error cancellation get controlled in our circuit for convex combinations of channels. Generally, we hope our work inspires new directions in error suppression in the transition from NISQ computing to FTQC, including both the theory and application of quantum error mitigation and the interplay between QEM and QEC [43].

We expect our circuit for implementing convex combinations of channels to find use in other applications, and to that end we have discussed on application in simulating open quantum systems. Even for a simple canonical example, we have seen that our circuit achieves a substantial reduction in both qubits and gate counts relative to current state-of-the-art methods. Our circuit is therefore a promising method for simulating and probing Markovian and non-Markovian evolution, an application with practical and theoretical value.

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More details on gate counts for Rabi flopping

Here we provide more details on computing the gate counts shown in Table. I. To solve (12), evolution under the Liouvillian is split into three channels:

$$\begin{aligned}
 \mathcal{O}_1[\rho] &= e^{-i\sigma_z\tau} \rho e^{i\sigma_z\tau} \\
 \mathcal{O}_2[\rho] &= e^{-i\sigma_x\tau} \rho e^{i\sigma_x\tau} \\
 \mathcal{O}_3[\rho] &= E_0 \rho E_0^\dagger + E_1 \rho E_1^\dagger
 \end{aligned}$$

each of which has the associated probability

$$\begin{aligned}
 p_1 &= \frac{\omega_0}{\lambda} \\
 p_2 &= \frac{\Omega}{\lambda} \\
 p_3 &= \frac{\gamma}{\lambda}
 \end{aligned}$$

where $\lambda = \omega_0 + \Omega + \gamma$ and $\tau = \lambda \delta t$, δt is the length of the time step, and the Kraus operators E_0 and E_1 correspond to an amplitude damping channel:

$$\begin{aligned}
 E_0 &= \begin{bmatrix} 1 & 0 \\ 0 & \sqrt{1-\beta} \end{bmatrix} \\
 E_1 &= \begin{bmatrix} 0 & \sqrt{\beta} \\ 0 & 0 \end{bmatrix}
 \end{aligned}$$

with $\beta = e^{-\gamma t}$. A circuit to simulate this channel using one ancilla qubit for dilation [30] is shown in Fig. 2.

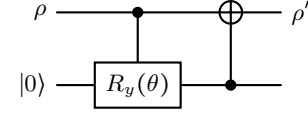


FIG. 2. A unitary circuit to implement the amplitude damping channel with input state ρ and output state ρ' . The damping parameter is $\beta = \sin^2(\theta)$ [30].

Using these channels, we implement both the method of [40, 42] and our method in Fig. 1 to simulate the evolution of (12). The resulting circuits are compiled to a gateset consisting of CNOT gates and arbitrary single-qubit rotations. These gate counts are shown in Table I.